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501.40141VX1

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03/03/2004

ANTONELLI, TERRY, STOUT & KRAUS, LLP
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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/658,396	09/10/2003	Hiroki Fujisawa	501.40141VX1	2256

TITLE OF INVENTION: SEMICONDUCTOR MEMORY DEVICE HAVING A HIERARCHICAL I/O STRUCTURE

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$300	\$1630	06/03/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
PHUNG, ANH K	2824	365-230030

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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2. For printing on the patent front page, list (1) the names of up to 3 registered patent attorneys or agents OR, alternatively, (2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.

Antonelli, Terry, Stout
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PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. Inclusion of assignee data is only appropriate when an assignment has been previously submitted to the USPTO or is being submitted under separate cover. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

01) Hitachi, Ltd.

02) Hitachi ULSI Systems Co., Ltd.

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Tokyo, Japan

Tokyo, Japan

Please check the appropriate assignee category or categories (will not be printed on the patent); ☐ individual ☒ corporation or other private group entity ☐ government

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(Authorized Signature)

Gregory E. Montone #28,141

(Date)

June 3, 2004

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06/04/2004 CHNGUYEN1 00000047 10658396

01 FC:1501
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